

101 098 659

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,790,663 B2
DATED : September 14, 2004
INVENTOR(S) : Robert Kerr et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page,

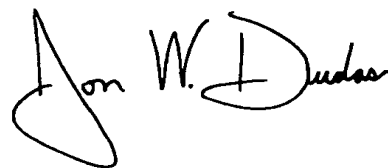
Item [56], **References Cited**, OTHER PUBLICATIONS, please insert -- Gray & Meyer, "Analysis and Design of Analog Integrated Circuits", Third Edition, John Wiley & Sons, Inc., 1993, pg. 169 --.

Column 2,

Line 65, please delete "array Diffusion" after "memory" and insert -- array. Diffusion --.

Signed and Sealed this

Nineteenth Day of April, 2005

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a cursive "Dudas".

JON W. DUDAS

Director of the United States Patent and Trademark Office